



MS ISSUE FEE  
PATENT  
0717-0448P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Hiroki DOI et al. Conf.: 3457  
Appl. No.: 09/696,006 Group: 2858  
Filed: October 26, 2000 Examiner: DONALD LAIR  
For: HALL DEVICE BIASING CIRCUIT AND MAGNETISM  
DETECTION CIRCUIT INCLUDING THE SAME

**COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


Sir:

The Examiner's statement of reasons for allowance points out features not taught by the prior art of record. However, Applicants note that it is the claims as a whole that define the scope of the invention for which protection is sought.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §1.16 or under 37 C.F.R. §1.17; particularly, extension of time fees.

Respectfully submitted,

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